



Figure 1

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21 current bus owner	22 next bus owner	23 number of misses	24 actual bus owner

14a

Figure 2

21 current bus owner	22 next bus owner	23 number of misses	24 actual bus owner
C	A	0	A
A	B	1	C
B	D	0	D
D	H	2	G
H	J	0	J
E	F	0	F
G	H	3	I
F	B	0	B
J	A	0	A

14a

Figure 3

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25 current sequence number	26 processor sequence	27 next sequence number	28 number of misses	29 actual processor sequence	14b

Figure 4

25 current sequence number	26 processor sequence	27 next sequence number	28 number of misses	29 actual processor sequence	14b
1	CACBA	1	0	CACBA	
2	BABDH	3	1	BABDE	
3	DADGE	5	0	DADGE	
4	HEHAC	9	2	HEHAB	
5	ACFEG	1	0	ACFEG	

Figure 5

